

REFLECTIONS  
ECE News Letter  
MVSR ENGINEERING COLLEGE  
(Sponsored by Matrusri Education Society - ESTD : 1980)

COMMITTEE

Chairman - Dr. M. V. Sridhar  
Secretary- Sri T. Jitendra Nath  
Treasurer - Sri M. V. Vasudeva Rao

---

Patron - Dr. V. Chandra Sekhar  
Editor - Dr. B. Sarala  
Editorial Committee  
Faculty - Smt. E.V. Naga Lakshmi  
          Smt. K.V.B.L. Deepthi  
Students - G. Sai Chandu  
          Sai Pavan  
          Kushbu Gupta

**Department Highlights**

1. Department has received ISO 9001:2008 certification for 3 Years.
2. Department Digital Library was created by Jagadish of III ECE
3. The department has received a Silver Medal from IEEE
4. Total 69% of students placed in different companies through campus recruitment.
5. B. Sarala awarded a PhD Degree.
6. DVSR Sasidhar awarded

**Editors Desk**

I have great pleasure in writing from Editor's desk on the activities of the Department carried out in the year. I congratulate DVSR Sasider, Assistant professor for achieving 'Flying officer Rank 'He led a team of 54 NCC candidates from different groups of Andhra & Telengana regions at all India VAYU DAYNIC camp. Congratulations Dr.B.Sarala on obtaining her Doctorate. Continuing with the advancement of Technology, several Technical papers were published in reputed Journals. SAMAVAR-THAN 2k16 was successfully organized by the Department with student participants from across the State. I am happy to inform that the Department has successfully undergrad ISO 9001:2008 certification for 3 years which has been possible due to the untiring support & commitment of the faculty & staff of our Department. I thank everyone for their support & I urge that this will be extended in the upcoming years as well.

## Journal papers published by faculty

S.No.	Name of the Staff	Title of the Paper	Name of Journal	Month & Year
1	B. Srinivas	Design and Analysis of Gate Diffusion input technology Techniques Based double gate Full Adder	International Journal of VLSI System Design and Communication Systems (IJVDCS)	Nov,2016
2	N. Namassivaya	Enhanced Low Power CMOS Current comparator using Diode Free Adiabatic (DFAL) Technique	International Journal of VLSI System Design and Communication Systems	Sep-2016
3	G.V.Chalapathi Rao	DEVELOPMENT OF A NEW EFFICIENT MULTIPATH MITTIGATION ALGORITHM FOR PRECISE GPS POSITION APPLICATIONS	The IUP Journal Of Telecommunications	Aug,2016
4	E.V.Naga Lakshmi	Design of ALU Using Modified GDI Technology	International Journal of Advance Research (IJOAR)	Aug,2016
5	Shilpa B Darvesh	Enhanced Multi-threshold Schmitt Trigger Circuit Design Using FINFET Technology (IJVDCS)	International Journal	Aug,2016
6	T. Kavitha	A Low Power 32 Bit Mac Unit Design Using Vedic Multiplier and Brent-Kung Adder	International Journal of Engineering Science and Generic Research (IJESAR)	Aug,2016
7	M.Sarath Chandra	Design & Implementation of Thermometer Code TPC for BIST	International Journal (IJETR)	Aug, 2016
8	T. Kavitha	DWT Based Scheme For Video Watermarking	International Research Journal of Engineering and Technology (IRJET)	2016
9	KVBL Deepthi	An investigation on Substrate Integrated Luneburg Lens Antenna with Gradient Index and Meta Materials	International Journal of applied engineering research ( IJAER)	July,2016
10	E.V.Naga Lakshmi	Implementation and Comparison of Power Gated CMOS Circuits	International Research Journal of Engineering and Technology (IRJET)	July,2016
11	SVR Manimala	Designing of Avionics Full Duplex Switch on Netfpga	IOSR Journal Electronics and Communication Engineering	July-Aug,2016
12	B. Bhavani	FPGA Based Performance Analysis of Micro programmed FIR filters using multipliers	International Journal of Engg. Science and Generic Research (IJESAR )	July-Aug,2016
13	B. Bhavani	LINUX Systems Development & Remote Debugging on Embedded Industrial Gate Way, IJIJR	International Journal of Innovative Technology & Research )	June-July,2016
14	N. Namassivaya	Design of 32 Bit High Speed Six Stage Pipeline RISC Processor for Convolution	International Journal of Engineering Science and Generic Research (IJESAR)	July-Aug,2016

15	Shilpa B Darvesh T. Kavitha	FPGA Implementation of High Performance Fully Pipelined Aes Algorithm Using Reversible Logic	International Journal of Engineering Science and Generic Research (IJESAR)	May-June, 2016
16	E.V.Naga Lakshmi	A New Design of Reversible Full Subtractor	International Journal of Educational Research	April, 2016
17	KVBL Deepthi	An Investigation of the Substrate-Integrated Luneburg Lens Antenna with Gradient Index and Meta-material Structures	International Journal of Applied Engineering Research	April- May, 2016

### Conference Papers Published by Faculty

S.No.	Name of the Faculty	Paper Title	Venue
1	Dr. T. Anil Kumar	A Review on Importance of Improving Spectral Efficiency in Massive MIMO communication	1 <sup>st</sup> National Conference on Computer Security, Image Processing , Graphics, Mobility & Analytics Organized by Dept. of CSE
2	N. Namassivaya	" GNSS and Focus on NAVIC	2 <sup>nd</sup> Annual General Meeting
3	E.V.Naga Lakshmi	Design of Reversible full subtract or using new reversible gate for low power applications	IEEE ocference - ICICT2016
4	T.Kavitha	An Efficient Compression Technique for ITU-T Group 3 Coded Images Using Variable Length Codes with Reduced Average Length	IEEE Conference - IICIP
5	Dr. N. Srinivas	Co-operative Cognitive Radio Network with Soft Data Fusion in Rayleigh Fading Channel	National Conference on Advanced Signal Processing Embedded Communication Systems (ASPECS-2016)
6	Dr. B. Sarala	Design and implementation of Symmetric Cryptographic Algorithm using 5x5 square matrix with Single Point Crossover	National Seminar
7	SVR. Manimala	Sparse Recovery Algorithms Based on Dictionary Learning for MR Image Reconstruction	IEEE Conference on WiSPNET- 2016
8	B. Sarala	Performance Analysis of Wavelet Preceding based Orthogonal Frequency Division Multiplexing over Mobile Radio Channels	National conference on Advances in Information Technology
9	B. Sarala	Design and Implementation of Modified Reed Solomon Error Correcting Codes	National conference on Advances in Information Technology
10	Dr. N. Srinivas	Performance of Weighted Fusion Based spectrum Sensing under Double Threshold in Cognitive Radio Network	International Conference on Microelectronics, Computing and

## **1. Department updates**

### **Guest lecturers / Training session / Seminars**

22-12-2016 Demo on RF Communication Lab Station-RF Transceiver Kit, RF Signal generator, vector network analyzer and spectrum analyzer".  
6-10-2016 Bathukamma Festival  
5-03-2016 Parent-Teacher Meeting ( sem I)  
15-02-2016 A Guest lecture on Feedback Amplifiers

### **Parent Teacher Meeting**

The Department has organized Parent Teacher meeting twice in a year i.e. once in each semester on 18<sup>th</sup> March 2017 and 28<sup>th</sup> October 2017. The main purpose of the Parent Teacher meeting was to create a common platform, where teacher and parents come together to enrich the student's educational experiences and discuss variety of issues, regarding all round development of students.

### **Alumni Meet**

### **Workshops / FDP**

1. A Five Day Intensive Course on "Probability Theory and Stochastic Processes"

4<sup>th</sup> -8<sup>th</sup>  
January 2016



2. 2- Day FDP Program on "Analog and Digital Circuit Design using Cadence"  
12<sup>th</sup> & 13<sup>th</sup>  
January ,2016
  
3. 2 day National Level Workshop  
(Zonal Round) on Android Application Development 5th & 6th Feb, 2016.
  
4. SAMAVARTHAN-2K16 10<sup>th</sup> & 11<sup>th</sup> March,2016



5. 2- day National Level workshop on Internet of Things (IOT) 18<sup>th</sup> & 19<sup>th</sup> March,2016
6. 2- day workshop on Biped ROBOT 16<sup>th</sup> & 17<sup>th</sup> Sept.,2016
7. 2-day Faculty Development Program on “ XILINX VIVADO DESIGN SUITE in Association with CoreEL Technologies 22<sup>nd</sup> & 23<sup>rd</sup> Sept.,2016
8. One-Week Faculty Development Program On Signal Processing Techniques 15<sup>th</sup> to 19<sup>th</sup> Nov,2016

### **Faculty Updates**

1. B. Sarala awarded a PhD Degree.
2. DVSR Sasidhar achieved “Flying officer Rank ‘.
3. Ms. M. Deepthi has joined the department as Asst. Prof. on 05/07/2016.
4. Smt. V. Rajitha Rani has joined the department as Asst. Prof. on 29/07/2016.

# 1. Student Achievements

## (i) Class Toppers

II ECE - Chakali Aishwarya

III ECE- Kannanedi Indira

IV ECE- Bongiri Akhila

## (ii) Gate Rankers

S.No	Name of the Student	GATE RANK
1	V. Abhishek	660
2	T Rathnam	
3		
4		

## GRE Rankers

S.No	Name of the Student	GRE SCORE
1	D.Chris Moses	308
2	S. Jagadeeshwara Prasad	305
3	K.Aishwarya	312
4	Ravi Sai Srinivas	308
5	Makarand P B	311
6	T Kapil Bharadwaj	321
7	N v S Madhu Kiran Harsha	316
8	G Harish	319
9	Vasudeva Madhusudan	315

## (iii) Prizes Won in Technical Events

S. No	Name of the student	Conf/ Fest organized by institutes	Event	Place	Date	Awards/ Prizes
1	Shaik Mohamud	IETE Student Branch	Paper Presentation	MVSR Engg. College	18 <sup>th</sup> Jan,2016	3 <sup>rd</sup>
2	S. Chandra Kiran	IETE Student Branch	Paper Presentation	MVSR Engg. College	18 <sup>th</sup> Jan,2016	3 <sup>rd</sup>
3	Ch. Pavana Rama 2451-13-735-111	TECHNOSTER	Techno Quiz	Sri Indu College of Engg. & Tech., Hyd	20 <sup>th</sup> & 21 <sup>st</sup> Feb,2016	2 <sup>nd</sup>
4	S.Dinesh	A National Level technical Symposium	Tech. Quiz	Sri Indu College of Engg & Tech.	20 <sup>th</sup> & 21 <sup>st</sup> Feb,2016	1 <sup>st</sup>

5	R. Dhana Sanketha	ARK techno solutions	NADC	IETE, MADRAS	5 <sup>th</sup> & 6 <sup>th</sup> Feb,2016	2 <sup>nd</sup>
6	S. Nisha	ARK techno solutions	NADC	IETE, MADRAS	5 <sup>th</sup> & 6 <sup>th</sup> Feb,2016	2 <sup>nd</sup>
7	M.Sai Krishna	CIENCIA 2k16	ELECTRONZEE	CVR Eng College	31 Mar & 1 <sup>st</sup> Apr '16	2 <sup>nd</sup>
8	A.Hari Priya	QUBIT,16	Paper Presentation	MGIT, Hyd	16 <sup>th</sup> March,2016	2 <sup>nd</sup>
9	M.Sai Krishna	SAMAVARTHA N2K16	Scintillation	MVSREC, Hyderabad	10-11 <sup>th</sup> March 2016	1 <sup>st</sup>
10	V.Prathik	SAMAVARTHA N2K16	Electro Paraphasing	MVSREC, Hyderabad	10-11 <sup>th</sup> March 2016	1 <sup>st</sup>
11	V. Nikhil Reddy	TECHVANZA2 K16	Paper presentation	KG Reddy Eng & tech	28 <sup>th</sup> & 29 <sup>th</sup> Sept 2016	1 <sup>st</sup>
12	Vamshi Krishna	IEEE DAY-2K16	JAM	MVSREC, HYD	1 <sup>st</sup> Oct 2016	2 <sup>nd</sup>
13	MD. Rafi	TECHNOZION -16	Avion-E	NIT, Warangal	22 <sup>nd</sup> October 2016	2 <sup>nd</sup>
14	Akhil Kadiyala	TECHNOZION -16	ARM Rover	NIT, Warangal	22 <sup>nd</sup> October 2016	3 <sup>rd</sup>
15	Asil Vineeth Chandra	TECHNOZION -16	ARM Rover	NIT, Warangal	22 <sup>nd</sup> October 2016	3 <sup>rd</sup>
16	Ch.Samrat Kautilya	TECHNOZION -16	ARM Rover	NIT, Warangal	22 <sup>nd</sup> October 2016	3 <sup>rd</sup>

#### (iv) Prizes won in Sports

S.No	Name of the Student	Sport Name	Event Details	Venue	Prize Won
1	Ch. Pavana Rama	Gully Cricket	National Annual sports meet Arena-16	Bits Pilani, Hyderabad Centre	2 <sup>nd</sup> Prize
2	V. Sai Krishna	Gully Cricket	National Annual sports meet Arena-16	Bits Pilani, Hyderabad Centre	3 <sup>rd</sup> Prize
3	K. Sharath Chandra	Carroms	National Annual sports meet Arena-16	Bits Pilani, Hyderabad Centre	2 <sup>nd</sup> Prize
4	A.Dileep	Carroms	National Annual sports meet Arena-16	Bits Pilani, Hyderabad Centre	2 <sup>nd</sup> Prize
5	Ch. Pavana Rama	Football	ATHELEMA-2K16	MVSR Engg. College	1 <sup>st</sup> Prize



## 2. Placement Record

year	No. of Companies Visited	No. Of Students Selected	Salary Per annum	
2016	30	117	IT 6.2 Lakhs Max (Deloit)	Core 7.00 MAX (FMC Ltd)